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(54) Electronic circuit package with diamond film heat conductor

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substrate. The edges of the diamond are brazed with a gold-indium braze in intimate thermal contact with a shoulder of the well to maximize heat conduction from the edges of the diamond into the base flange. The base flange may be fastened to the flat surface of a heat sink member with the diamond in intimate thermal contact with the heat sink member.

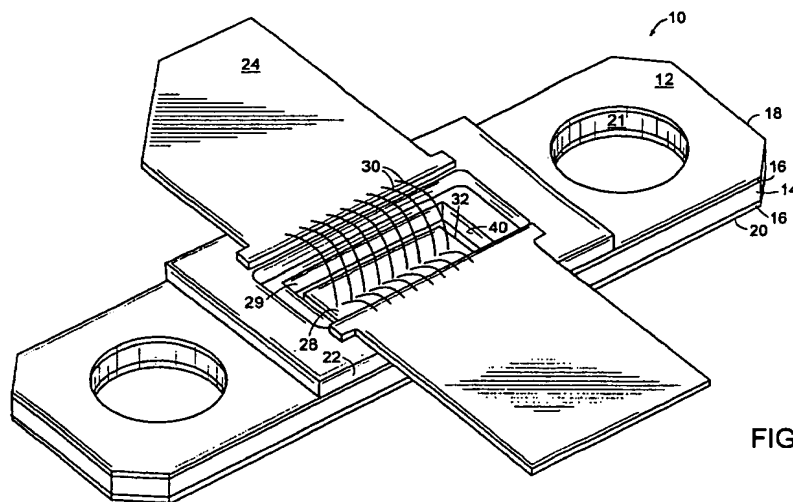


FIG. 2

EP 0 932 199 A2

Description

Field of the Invention

[0001] The invention relates generally to packages for electronic circuits and in particular to such packages which make use of diamond film for conducting heat from the device to the outside of the package.

Background of the Invention

[0002] Electronic devices are typically housed in a "package". The package is designed to provide the device protection from environmental factors, such as moisture. It is also designed for convenient electrical interconnection and for conduction of heat generated by the circuit to the outside of the package, where it may be removed by various means. There are many different types of packages commercially available to the manufacturers of electronic circuits. To some extent the wide use of certain types has given them the reputation as a "standard" design geometry identified by a designation recognized in the industry through validation by trade organizations, such as the IEEE (Institute of Electronic and Electrical Engineers).

[0003] Heat removal from circuit devices in packages has become increasingly challenging as their power densities have increased. For example, large scale integrated circuits include ever larger numbers of active elements and operate at ever higher speeds, which requires more power. Power amplifiers have been reduced in size, to that they have higher power densities. More effective heat removal has been accomplished by the incorporation into the package design of materials which have a high heat conductivity, such as copper. Usually, the copper is in the form of a substrate of the package, and the circuit device is mounted directly to it with a suitable bonding agent. In some cases the circuit device cannot be mounted directly on an electrically conducting member because spurious noise in its signal would arise due to capacitive coupling to the substrate. In that case either the conductive substrate must be covered with an electrically insulating layer before the circuit device is attached, or the substrate must itself be made of an electrically insulating, thermally conductive material. Packages of this general type are described, for example, in U.S. Patents 5,065,281 and 5,109,268.

[0004] While there are a number of materials, such as aluminum nitride, beryllium oxide, aluminum oxide, and silicon nitride, which are electrically insulating and also effective conductors of heat, by far the most outstanding material in this category is diamond. In particular, diamond film can be made by CVD (chemical vapor deposition) in wafer form suitable for use as a substrate material. It has a very high electrical resistivity and is about three to five times as effective in conducting heat as is even copper, the best of the metals in this regard.

[0005] The incorporation of diamond film into a package for heat removal involves a number of difficulties. Among these is that the CTE (coefficient of thermal expansion) of diamond is much lower than that of the metals typically used in packages. This can lead to extreme stresses and eventual mechanical failure of the package itself or of the circuit device housed by it. Attempts to alleviate mismatching of the CTE often lead to multi-layered structures in which the heat conductivity of the diamond material is not used to full advantage due to the thermal impedances introduced by intervening interfaces or the external geometry of the package becomes altered, so that it can no longer be used in the same way for all applications. Such a change in geometry is often unacceptable to the circuit device manufacturers who use the package because it requires specialized assembly equipment and processes.

[0006] There is a need for a circuit package which successfully incorporates diamond film into a circuit device package for heat removal while avoiding damaging thermal mismatch with other materials attached to it and minimizing any detraction in its performance for removing heat from the circuit device to the outside of the package.

Summary of the Invention

[0007] In accordance with the novel invention as described herein, diamond is incorporated in a package in such a manner that the circuit device may be thermally coupled directly to the surface of it inside the package, while heat may be removed directly from a surface of the diamond outside the package. This structure provides a rapid thermal response because of the low heat capacity of the diamond, while eliminating all other interfaces between the circuit device and the outside diamond surface to maximize thermal conductivity. As a result of the highly effective heat removal of this structure, the operating temperature of the circuit device can be stabilized at a sufficiently low temperature that CTE mismatch problems between the circuit device and the diamond are avoided.

Brief Description of the Drawings

[0008]

FIGURE 1 is an elevated perspective view of an electronic circuit device package in accordance with one embodiment of the present invention in its sealed condition.

FIGURE 2 shows the package of Fig. 1 with its cap removed to expose its inner chamber.

FIGURE 3 is a sectioned view of the package of Figs. 1 and 2 without the circuit device and connecting wires.

FIGURE 4 is an enlarged, sectional view of a portion of the section of Fig. 3 showing details of a

brazed joint.

Detailed Description

[0009] In accordance with a preferred embodiment of the present invention, a standard geometry circuit device package 10 as shown in Figs. 1-4 which is identified in the industry by the designation RF701 and manufactured, for example, by the Brush Wellman Engineered Materials Company of Newburyport, Massachusetts, is adapted to incorporate a CVD diamond film heat conducting element. This package is especially suitable for housing RF (radio frequency) devices which have a relatively high power output.

[0010] The main supporting structure of the package 10 is a metal laminate base flange 12 which is adapted to be fastened to a heat sink member (not shown). The base flange 12 is a laminate which includes a core 14 of molybdenum about 24 mils thick bonded by a heat diffusion process directly to a copper cladding 16 about 8 mils thick over both its top face 18 and its bottom face 20. Bonded to the copper of the top face 18 is an alumina frame 22 to which there are attached on opposite top sides two copper power leads 24 which pass from the outside to the inside of the package 10. A cap 26 hermetically seals the package 10.

[0011] The base flange 12 is a modified version of what is normally CuW (copper-tungsten) base flange for this type of package. Normally, such a flange is entirely of CuW. It is designed to have its bottom face 20 firmly fastened to the flat surface of a heat sink member, such as a metal "slug", by means of screws which extend through two fastening holes 21. The present, modified version of the base flange 12 is made as a laminate because the molybdenum core has a CTE which closely matches that of the silicon circuit device to be housed in the package. The copper cladding is provided to permit assembly of the package components in the same manner as if the base flange 12 were copper-tungsten, thus eliminating the need for developing specialized machinery and procedures for this process.

[0012] Fig. 2 shows the package of Fig. 1 with the cap 26 removed to reveal a silicon circuit device 28 disposed in a chamber 29 formed by an opening 40 through the base flange 12. The device 28 is electrically connected to the two copper power leads 24 by means of a plurality of bonding wires 28 and is attached by a thermally-conductive bonding agent to a heat conducting floor member, or substrate 32 made of diamond film about 350 micrometers thick. The diamond substrate 32 to which the device 28 is attached is metallized over its entire surface with first a titanium layer about 1000 Angstroms thick, then a layer of platinum about 2000 Angstroms thick, and then a layer of gold about 3.75 microns thick. The attachment is carried out by heating the package and pressing the device 28 against the metallizing 38 while vibrating it horizontally to form a gold-silicon eutectic interface and is a known process. Such an

interface provides an intimate thermal and electrical contact. The bottom of the device 28 has a base electrical lead which becomes connected to the base flange via the metallizing 38.

[0013] The Fig. 3 shows the package 10 in section, with the cap 26, device 28, and bonding wires 30 removed. It is seen that the diamond substrate 32 is inlaid into a shallow well 34 formed in the bottom surface of the base flange 12, so that it abuts a shoulder 36. As is shown in greater detail in Fig. 4, the substrate 32 is brazed to the shoulder 36 by means of a gold-indium braze 37 which is 81 weight % gold and 19 weight % indium and is sufficiently malleable to accommodate the small degree of CTE mismatch between the diamond substrate 32 and the molybdenum of the base flange 12. The well 34 is made just deep enough so that the diamond substrate 32 fits into it to become flush with the bottom surface 20 of the base flange 12. When the base flange is fastened to a heat sink member (not shown), with an appropriate thermal interface compound, such as a thermal grease (e.g. a wax loaded with finely-divided thermal conductor, such as diamond or boron nitride), the diamond substrate 32 makes optimum intimate contact with the heat sink for maximizing heat transfer.

[0014] In practice, the thickness of the diamond is chosen to suit the needs of the circuit in terms of how much heat spreading and how much total heat conduction rate is required. The well 34 is then dimensioned to accommodate those dimensions of the diamond substrate 32. The calculations for such design parameters are known to those in the art.

General Considerations

[0015] While in the embodiment described above, the core of the base flange is molybdenum and the substrate is diamond, the invention is applicable to other choices of these materials and need not be a laminate at all. For example, the base flange could be entirely of CuW, entirely of tungsten, or entirely of aluminum silicon carbide. However, for base flange materials which present a greater CTE mismatch problem for the device being housed, it will be necessary to limit more severely the temperature excursions of the device.

[0016] The attachment of the substrate to the shoulder of the base flange can be by other bonding methods, depending upon their suitability otherwise for the particular structure involved. The braze 37 used in the package 10 forms a particularly intimate thermal contact between the members and has excellent mechanical integrity. This leaves the substrate with not only a very high heat flux passing through its thickness to the outside, but also a very high heat flux passing to the base flange member from its edges where they are brazed. In some applications, the heat transfer from the edges may be less important. In that case other bonding agents, such as solders or epoxies, may be sufficient.

An alternative braze which could be used in place of the gold-indium combination would be one of 20 weight % copper and 80 weight % silver. In that case the metallizing would be replaced by one which has a first layer of titanium about 1000 Angstroms thick, then a layer of tungsten about 2000 Angstroms thick, then a layer of platinum about 2000 Angstroms thick, and finally a layer of nickel about 2 microns thick.

[0017] For situations in which removal of heat from the edges of the diamond substrate is not considered important, the means of attaching the substrate to the base flange is less important, and can be accomplished, for example, without any metallizing and by simply a suitable organic adhesive. Likewise, where the heat removal from the device to the diamond substrate need not be maximized, there may be used other means of fastening the device to the substrate which do not require metallizing.

[0018] The details of the leads and how they are held in place on the package and connected to the housed device are not considered a part of the invention and may be a matter of choice. Likewise, the manner in which the housing is sealed is a matter of choice. It is of key importance, however, that the device be directly thermally coupled to one side of the diamond substrate and that the diamond substrate be directly thermally coupled to a heat removal means. By "directly thermally coupled" is meant that the only intervening material, if any, between members so coupled is a thermal coupling agent which is viscously deformed by pressure between the members and has a resulting thickness less than 10 microns. It does not include a structure in which a solid polyimide or other film of about 25 microns or more in thickness is interposed between the members. It does include the use of a settable adhesive, solder, braze, or thermal grease.

[0019] If the package is housing a device which does not require attachment to a conductive substrate for providing an electrical lead connection, then the diamond substrate can have metallizing on its inside surface only where needed for brazing and for attaching the device. On its outside surface, it then needs to have metallizing only as required for providing a good thermal coupling to whatever means are used for removing the heat conducted to that outside surface.

[0020] Rather than be inset into a shallow well, as is the case for the diamond substrate 32 of the package 10, the diamond substrate could also be simply be attached to the bottom surface of the base flange, overlapping the edge of an opening in the flange for forming a housing chamber, so long as this suits the needs of the heat removal means for removing heat from the outside surface of the diamond. For example, in such a case it may be necessary to provide spacers of the same thickness as the diamond substrate under the holes for fastening the package to a heat sink in order to prevent bending of the base flange as a result of a cantilever effect from the diamond substrate being raised

above the bottom surface of the base flange.

[0021] The invention can be adapted to a wide variety of packages which feature a base flange which is designed to act as a heat removal member for the housed device. the adaptation involves removing at least that portion of the base flange which would support the device and replacing it with diamond film substrate of a thickness which meets the demands of thermal management of the device. In addition, the diamond film substrate should be located so that it properly couples the output heat of the package to heat removal means. If the package is to be mounted to a solid heat sink, it may be necessary to have the substrate flush with the lower surface of the base flange to be mounted. If the heat removal is by fluid flow or liquid spray, this may not be necessary.

Claims

1. A package for housing an electronic circuit device, comprising:
 - a base flange adapted to be fastened to a heat removal means, the base flange having a top face and a bottom face;
 - an opening between the top face and bottom face of the base flange;
 - a diamond substrate having a top and a bottom major surface disposed in the opening and fastened to the base flange about its perimeter.
2. The package in accordance with claim 1 wherein the base flange has a rim at its bottom face which includes a shoulder having a depth from the bottom face of the base flange.
3. The package in accordance with claim 2 wherein the diamond substrate is inlaid on the shoulder.
4. The package in accordance with claim 3 wherein the bottom major surface of the diamond substrate is flush with the bottom face of the base flange, and
5. The package in accordance with claim 4 wherein a bonding agent is provided between the top major surface of the diamond substrate and the surface of the shoulder.
6. The package in accordance with claim 5 wherein the bonding agent is a braze.
7. The package in accordance with claim 6 wherein the braze is a gold-indium braze.
8. The package in accordance with claim 7 wherein the braze is 81% by weight of gold and 19% by weight of indium.

9. The package in accordance with claim 1 wherein the base flange is made of primarily a metal having a significantly lower coefficient of thermal expansion than does copper.

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10. The package in accordance with claim 9 wherein the base flange metal is molybdenum.

11. The package in accordance with claim 10 wherein at least the bottom face of the base flange is covered with a surface layer of a different material.

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12. The package in accordance with claim 11 wherein the different material is a metal having a coefficient of thermal expansion significantly higher than that of molybdenum.

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13. The package in accordance with claim 12 wherein the different material is copper.

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14. The package in accordance with claim 1 wherein the diamond substrate is metallized on its upper major surface at least over an area in which an electronic circuit device is to be attached.

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15. The package in accordance with claim 14 wherein the metallizing includes a first layer of titanium on the diamond.

16. The package in accordance with claim 15 wherein the titanium layer is about 1000 Angstroms thick.

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17. The package in accordance with claim 16 wherein the titanium layer is covered with a layer of platinum.

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18. The package in accordance with claim 17 wherein the platinum layer is about 2000 Angstroms thick.

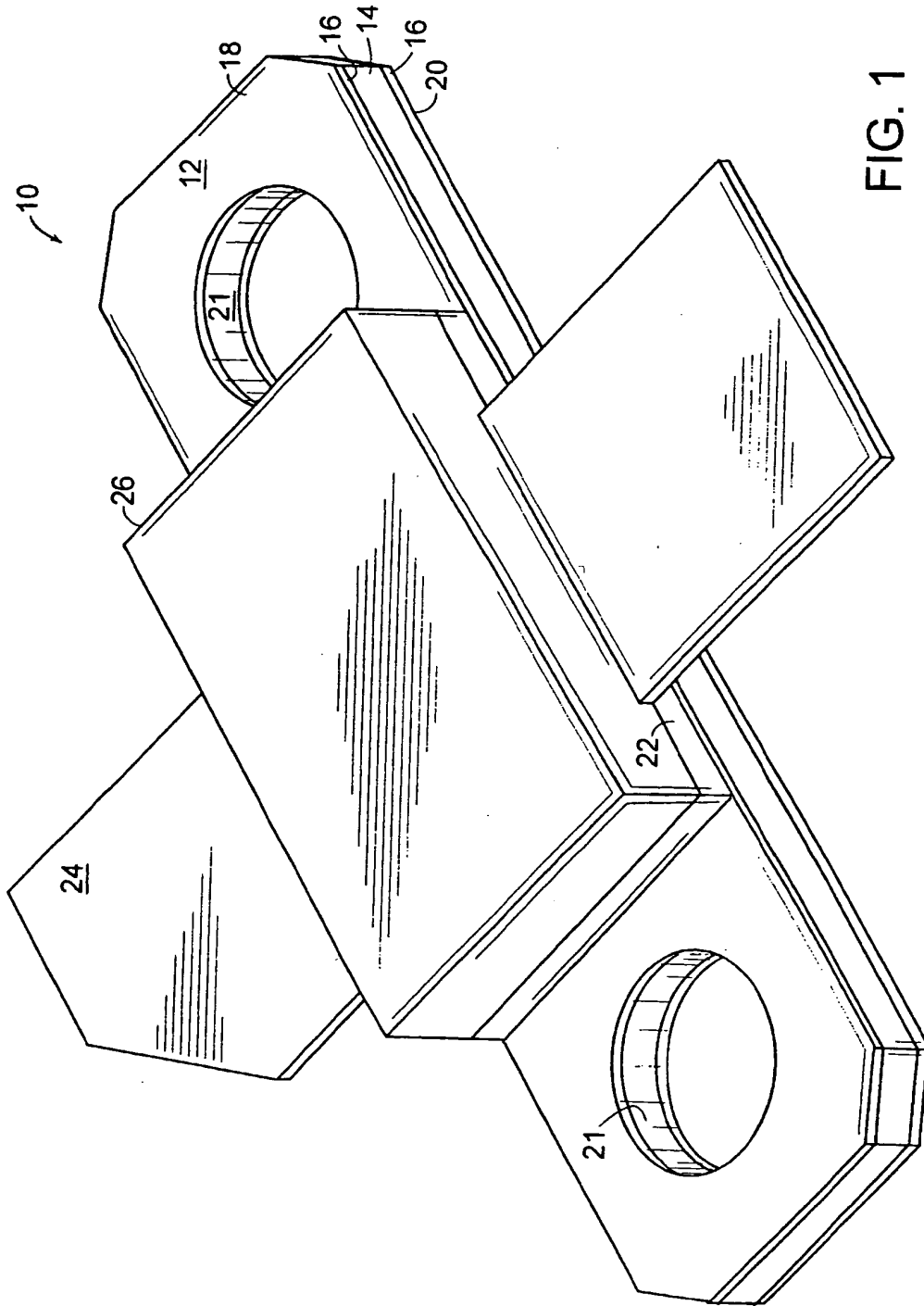
19. The package in accordance with claim 18 wherein the platinum layer is covered with a layer of gold.

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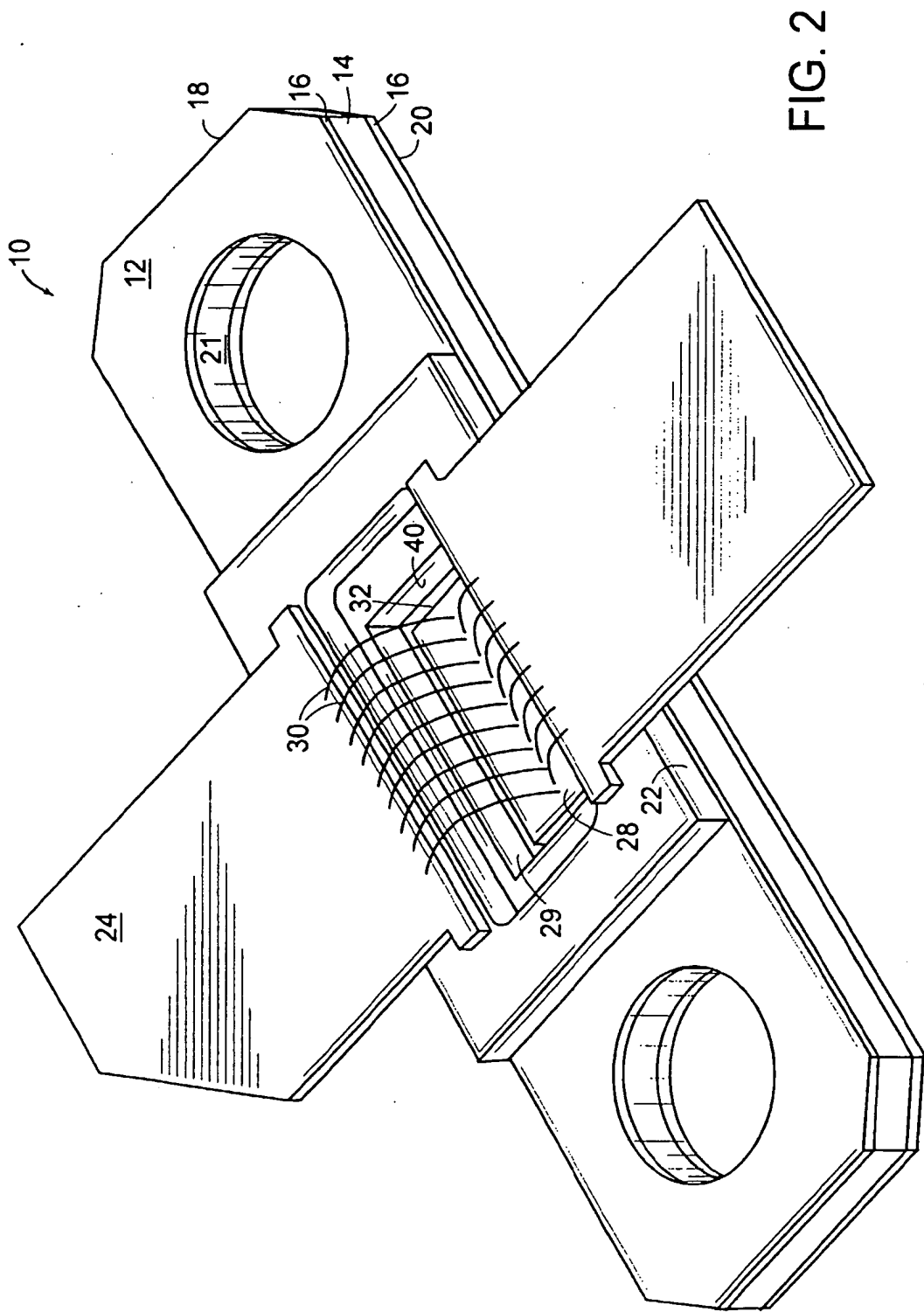


FIG. 2

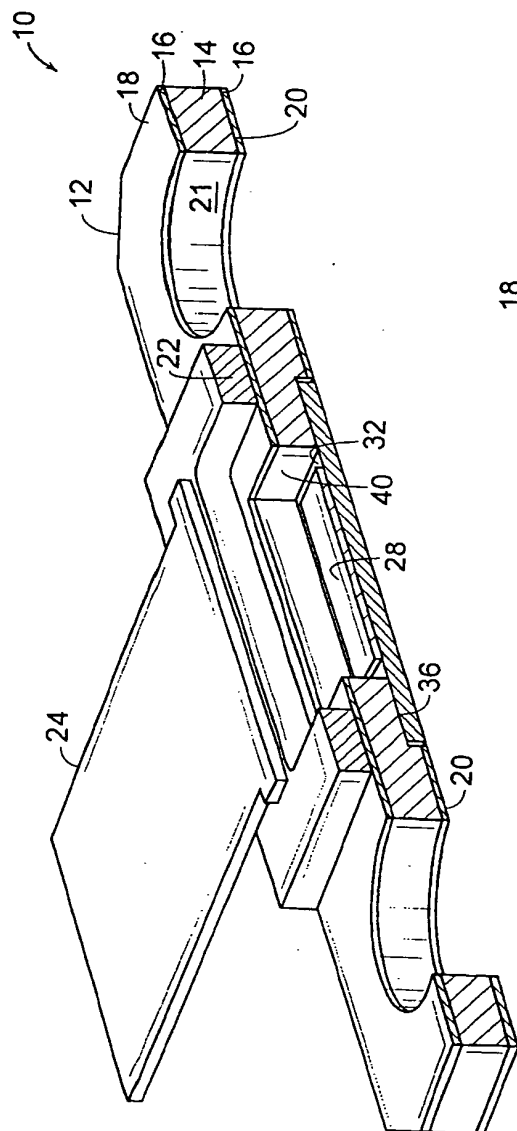


FIG. 3

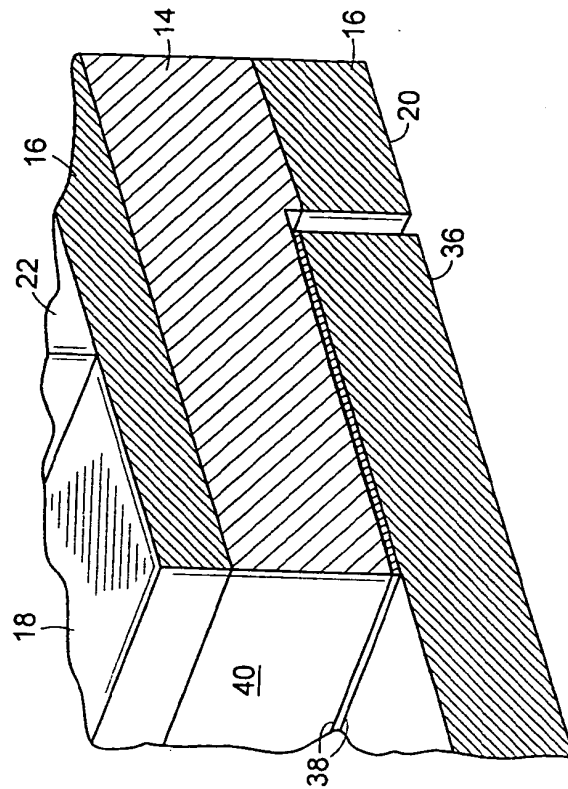
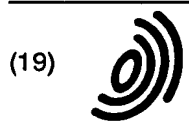


FIG. 4



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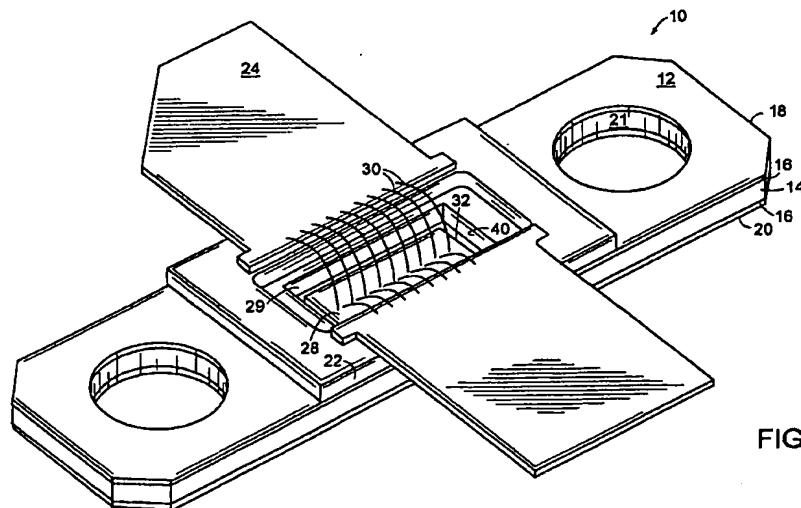


FIG. 2

EP 0 932 199 A3



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EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	WO 97 05757 A (CRYSTALLINE MATERIALS CORP ; LEWIS ELLIOTT (US); KOBA RICHARD (US);) 13 February 1997 (1997-02-13)	1, 9, 10	H01L23/373 H01L23/66 H01L23/04 H01L23/06 H01L23/057
Y	* page 16, line 17 - page 27, line 18 *	2-7, 14-19	
	* page 42, line 1 - page 43, line 18; figure 3 *		
Y	EP 0 427 265 A (IBIDEN CO LTD) 15 May 1991 (1991-05-15) * figure 20 *	2-7	
Y	EP 0 637 076 A (MOTOROLA INC) 1 February 1995 (1995-02-01) * the whole document *	14-19	
A	EP 0 585 084 A (AMERICAN TELEPHONE & TELEGRAPH) 2 March 1994 (1994-03-02) * the whole document *	15-19	
			TECHNICAL FIELDS SEARCHED (Int.Cl.8)
			H01L
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 3 July 2000	Examiner Prohaska, G
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9705757 A	13-02-1997	NONE	
EP 0427265 A	15-05-1991	JP 2813682 B	22-10-1998
		JP 3152985 A	28-06-1991
		DE 69013462 D	24-11-1994
		DE 69013462 T	18-05-1995
		US 5124884 A	23-06-1992
EP 0637076 A	01-02-1995	US 5388027 A	07-02-1995
		CN 1098844 A,B	15-02-1995
		JP 7058257 A	03-03-1995
EP 0585084 A	02-03-1994	US 5234153 A	10-08-1993
		CA 2099049 A	01-03-1994
		DE 69321904 D	10-12-1998
		DE 69321904 T	12-05-1999
		JP 2837616 B	16-12-1998
		JP 6177135 A	24-06-1994
		SG 43769 A	14-11-1997

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